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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/572,866	04/05/2006	Jurgen J.L. Hoppenbrouwers	GB 030184	2099
24737	7590	10/01/2008	EXAMINER	
PHILIPS INTELLECTUAL PROPERTY & STANDARDS			SADIO, INSA	
P.O. BOX 3001			ART UNIT	PAPER NUMBER
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10/01/2008	PAPER			

**Please find below and/or attached an Office communication concerning this application or proceeding.**

The time period for reply, if any, is set in the attached communication.

<b>Office Action Summary</b>	<b>Application No.</b>	<b>Applicant(s)</b>	
	10/572,866	HOPPENBROUWERS ET AL.	
	<b>Examiner</b>	<b>Art Unit</b>	
	INSA SADIO	2629	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

#### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

#### Status

1) Responsive to communication(s) filed on 05 April 2006.  
 2a) This action is FINAL.                    2b) This action is non-final.  
 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

#### Disposition of Claims

4) Claim(s) 1-12 is/are pending in the application.  
 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.  
 5) Claim(s) \_\_\_\_\_ is/are allowed.  
 6) Claim(s) 1-12 is/are rejected.  
 7) Claim(s) \_\_\_\_\_ is/are objected to.  
 8) Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

#### Application Papers

9) The specification is objected to by the Examiner.  
 10) The drawing(s) filed on \_\_\_\_\_ is/are: a) accepted or b) objected to by the Examiner.  
     Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
     Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).  
 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

#### Priority under 35 U.S.C. § 119

12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  
 a) All    b) Some \* c) None of:  
     1. Certified copies of the priority documents have been received.  
     2. Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.  
     3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

#### Attachment(s)

1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)	4) <input type="checkbox"/> Interview Summary (PTO-413)
2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)	Paper No(s)/Mail Date. _____ .
3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08)	5) <input type="checkbox"/> Notice of Informal Patent Application
Paper No(s)/Mail Date <u>08/17/07</u> .	6) <input type="checkbox"/> Other: _____ .

## DETAILED ACTION

### ***Claim Rejections - 35 USC § 102***

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

**Claims 1-4, 5-12** are rejected under 35 U.S.C. 102(b) as being anticipated by Kanauchi et al. (US Publication number 2003/0197472), hereinafter referenced as Kanauchi.

**Regarding claim 1**, Kanauchi discloses Drive unit and drive method of light-emitting display panel. Further, Kanauchi discloses wherein said A method of illuminating an active matrix electroluminescent display device comprising an array of display pixels arranged in rows and columns (see Fig. 5), the method comprising, at any point in time, illuminating a plurality of rows of pixels, the plurality of rows defining at least two bands (10) of rows separated by a non-illuminated band (12), the at least two bands (10) of rows of pixels scrolling in the column direction over time (see Fig. 12), and wherein at most 75% of the rows are illuminated at any point in time (see paragraphs [0072], [0074], (equivalent to partial display)).

**Regarding claim 2**, Kanauchi discloses everything as applied above in claim1. Further, Kanauchi discloses wherein said each band (10) of rows of pixels comprises a plurality of adjacent rows of pixels (see paragraph [0072], display region).

**Regarding claim 3,** Kanauchi discloses everything as applied above in claim1 or 2. Further, Kanauchi discloses wherein said image data for different frames of the image to be displayed are displayed in the different bands (see paragraph [0078], [0079]).

**Regarding claim 4,** Kanauchi discloses everything as applied above in claim1. Further, Kanauchi discloses wherein each band (10) of rows of pixels comprises a plurality of sequential alternate rows of pixels (see paragraph [0072], [0073]).

**Regarding claim 6,** Kanauchi discloses everything as applied above in any preceding claim. Further, Kanauchi discloses wherein said at most 50% of the rows are illuminated at any point in time (see paragraphs [0072], [0074], (equivalent to partial display)).

**Regarding claim 7,** Kanauchi discloses everything as applied above in claim 6. Further, Kanauchi discloses wherein said at most 30% of the rows are illuminated at any point in time (see paragraphs [0072], [0074], (equivalent to partial display)).

**Regarding claim 8,** Kanauch discloses Drive unit and drive method of light-emitting display panel. Further, Kanauchi discloses wherein said An active matrix electroluminescent display device comprising an array of display pixels (1) arranged in rows and columns, and row driver circuitry (8) for illuminating a plurality of rows of pixels simultaneously (see Fig. 5), the plurality of rows defining at least two bands (10) of rows separated by non- illuminated bands (12), wherein the row driver circuitry comprises means for illuminating each row for at most 75% of the frame period, such that the

illuminated rows define at least two bands (10) of rows of pixels which scroll in the column direction over time (see Fig. 12).

**Regarding claim 9,** Kanauchi discloses everything as applied above in claim 8.

Further, Kanauchi discloses wherein said further comprising a frame buffer (22) for storing image data (see Fig. 2 [data driver]).

**Regarding claim 10,** Kanauchi discloses everything as applied above in claim 9. Further, Kanauchi discloses wherein said the frame buffer (22) stores an amount of data corresponding to a single frame of image data (see paragraph [0073], [0076], [0042], Fig. 13).

**Regarding claim 11,** Kanauchi discloses everything as applied above in claim 10. Further, Kanauchi discloses wherein said data is written into the frame buffer (22) progressively frame by frame in sequence, such the frame buffer (22) stores partial data for two adjacent frames, and wherein data is read out from the frame buffer at two locations simultaneously (see paragraph [0073], [0076], [0042], Fig. 13).

**Regarding claim 12,** Kanauchi discloses everything as applied above in claim 10. Further, Kanauchi discloses wherein said the two locations contain data from different adjacent frames of image data (see paragraph [0073], [0076], [0042], Fig. 13).

#### ***Claim Rejections - 35 USC § 103***

2. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

**Claim 5** is rejected under 35 U.S.C. 103(a) as being unpatentable over Kanauchi in view of Morita (US Publication number 2002/0196241).

**Regarding claim 5**, Kanauchi discloses everything as applied above in claim 4.

However Kinauchi fails to disclose wherein said **one band of rows comprises only odd rows and another band of rows comprises only even rows**.

In a similar field of endeavor Morita discloses a Scan-driving circuit, display device, electro-optical device, and scan-driving method. In addition, Morita discloses **one band of rows comprises only odd rows and another band of rows comprises only even rows** (see paragraph [0193], [0213]).

Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to modify Kanauchi by specifically providing **one band of rows comprises only odd rows and another band of rows comprises only even rows**, as taught by Morita, for the purpose of simultaneously drive the scan lines for some polarized voltages.

### ***Conclusion***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to INSA SADIO whose telephone number is (571)270-5580. The examiner can normally be reached on MONDAY through FRIDAY 8am to 5pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Amare Mengistu can be reached on 571-272-7674. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

INSA SADIO  
Examiner  
Art Unit 2629

/Amare Mengistu/  
Supervisory Patent Examiner, Art Unit 2629

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